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SEAT No.\_\_\_\_

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SARDAR PATEL UNIVERSITY
M.Sc. (PHYSICS) I<sup>st</sup> Semester Examination
Monday, 16<sup>th</sup> April, 2018 2:00 pm to 5:00 pm
Course No.: PS01CPHY23: Analog and Digital Electronics

All	questions	are	compu	lsory.
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Total Marks:70

	le choice questions.
(1) Whic	of the following devices has more than one PN-junction?
(a) L	(b) Solar-Cell (c) SCR (d) UJT
	verse recovery time of a diode arises due to the
` '	ority carriers in the depletion-region
	nority carriers in the n- & p-regions
	ority carriers in the n- & p-regions
	ority carriers in the depletion-region
	clipper circuits
, (a) c	nge the shape of input wave (b) do not change the shape but shift
the le	el of input wave (c) do both a and b as per requirement
(d) N	
	ive recombination of charge carriers take place in case of LED if it
ì is ma	e un from
(a) T	ect band-gap semiconductor (b) Indirect band-gap semiconductor
	th a and b (d) None
	n of the following is a cyclic or unit distance code?
(a) 8	21 (b) Gray code (c) Excess-3 code (d) ASCII code
(6) A ce	ain binary counter circuit uses four Flip-Flops. What will be the
	us of this counter?
(a) 1	(b) 32 (c) 512 (d) 256
	of the following is an example of sequential circuits?
(a) A	<del>-</del>
` ,	otractor (d) Comparator
	ounter type ADC is also known as aADC
` '	acking type (b) Flash type
(c) S	ccessive approximation type (d) Digital Ramp type
(-) -	
Q.2 Sho	answer questions.(Attempt any seven)
	entiate between phototransistor and photodiode.
	ss the working action of diode as a switch.
	OPAMP is also known as differential amplifier?
	is the difference between comparator and Schmitt trigger?
	are sequential codes?
	are sequential codes:
(6) State	advantages and limitations of parallel and serial counters.
(7) Defi	advantages and limitations of parallel and serial counters.  e propagation delay time and glitch in a counter circuit.  is Digital to Analog convertor? Mention its important applications.

(9)	Distinguish between volatile and non-volatile semiconductor memory.		
Q.3(a) (b)	Write a note on LED.  Discuss in detail about the diode clamper circuits.  OR		
(b)	Explain the construction and working principle of TRAIC. What are its applications?		
Q.4(a)	Why IC-741 is known as Op-Amp? Sketch its block diagram and explain its working.	(6)	
(b)	Construct an astable multivibrator circuit using IC-555 and explain its working.	(6)	
	OR		
(b)	What is Voltage Control Oscillator (VCO)? Construct a VCO circuit using IC-741 and explain its working.	(6)	
Q.5(a)	Differentiate between weighted and non-weighted codes and discuss any two BCD codes in detail.	(6)	
(b)	Explain working of a full adder circuit with the help of a logic diagram and truth table.	(6)	
	OR		
(b)	Differentiate between synchronous and asynchronous counter and explain the working of any one of this 4-bit up-counter.	(6)	
Q.6(a)	Define resolution and accuracy of a D to A convertor. Also find the percentage resolution of a 12 bit DAC. What will be the full-scale output voltage if it has the step size of 50 mV?	(6)	
(b)	The state of the s	(6)	
(b)	OR	. ,	
(b)	the state of the s	(6)	