BILL

SARDAR PATEL UNIVERSITY

MSc IT (Integrated) (CBCS) Semester - 4 Friday, Date: 1st April, 2016

Session: Morning Time: 10:30 A.M. to 1:30 P.M.

Course Code: PS04CIIT01

Course Title:	Operating	Systems – II	Total Marks: 70
	-		

		ple Choice Questions			[10]			
			numh	er of small holes is known as				
	The memory storage broken in number of small holes is known as							
	Α.	Page	. C. I	rames				
	B.	Fragmentation	D. 8	Segmentation				
2.		rnal fragmentation problem						
		Virtual Memory Segmentation		Demand Paging Compaction				
	Best	fit and first fit strategies su	affer f	rom the problem of				
	Α.	Delay	C.	Poor Performance				
	В.	External Fragmentation	D.	Internal Fragmentation				
		data structure is u	ised to	implement second chance				
		rithm.						
		Circular Queue.						
		Stack		Linked List				
		swaps page from me						
		Pager		MMU Sahadular				
		Lazy swapper						
		cal file system manages						
	Α.			Metadata				
	В.	Device driver		Boot				
	Set o	of operations for performing	g a spe	ecific task is known as				
	A.	Code		Program				
	В.	Transaction	D.	All of these				
	Part	ition containing no file syst	em is	known as disk.				
	A.	Vnode		Mount				
	В.	Boot loader	D.	Raw				
		connects slow dev	vices t	o the PCI Bus.				
		DMA		Expansion bus				
	B.	Controller	D.	PCI Express				
	Devi	ce connects with computer	via					
	A.	Ports	C.	Register				
	B.	Cache	D.	Bus				

	Q2.	Answer the following short questions (Attempt any TEN)	[20]
	1.	Differentiate between: Static linking and Dynamic linking?	
	2.	What is Cache memory?	
	3.	What is stub?	
	4.	What is virtual memory? State its advantage.	
	5. 6.	What is virtual address space? Draw diagram for the same.	
	о. 7.	List all page replacement algorithms.	
	8.	Draw diagram of typical FCB.	
	9,	List different allocation methods.	
	10.	What is hash table? State its disadvantage. Define: Bus	
	11.	What is the job of interrupt handler?	
	12.	List basic functions of hardware clocks and timers.	
	Q3.	What is fragmentation? Explain types of fragmentation in detail.	[10]
	Q3.	Explain paging model of memory management in detail.	[10]
	Q4.a.	Explain FIFO Page replacement algorithm.	[05]
	b.	Write short note on virtual memory.	[05]
	Q4.a.	OR Evaluin stans involved in any facility is	
	b.	Explain steps involved in page fault with diagram.	[05]
	υ.	Explain LRU page replacement algorithm.	[05]
	Q5.a.	What is need of backup? Explain its types in detail	[05]
	b.	Write short note on Directory implementation. OR	[05]
	Q5.a.	Explain linked allocation method in detail.	[05]
	b.	State problems with consistency - checking approach? Also state solution for the same.	[05]
(Q6. a.	Explain the concept of interrupts.	[06]
	b.	Explain in detail Direct memory access with steps of DMA transfer.	[04]
C	Q6. a.	Explain the concept of I/O hardware.	[06]
	b.	Explain different types of devices according to its characteristics.	[04]
		The special devices according to its characteristics.	1041