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SARDAR PATEL UNIVERSITY

External Examination

M.Sc-Information Technology (Integrated) - SEMESTER-I

Course No: PS01CIIT02 -Subject : Computer Organization

	: 10:30 a.m to 01:30 p.m		Marks: 70
.1	Multiple choice questions.		[10
	i performs operations	such as addition and Boolean AND needs	ad to
	our y out the mondellons.	South I I I I I I I I I I I I I I I I I I I	CG 10
	(a) Arithmetic Logic Unit	(b) Registers	
	(c) Control Unit	(d) None of above	
	ii. In Hexadecimal Number system	m B stands for	
	a) 10	b) 11	
	c) 12	d) None of above	
	iii. Secondary storage of a comput		
	 Secondary storage of a comput a) Hard Disk 	er is also known as	
	c) Main Memory	b) Auxiliary storage	
	c) Main Memory	d) None of above	
i	v. The OR gate has two or more in	nput signals. If any input is, the	output
		, —, uie	output
	a) high	b) low	
	c) both A and B	d) none	
V	. ASCII stands for:		
	America standard coded infor	mation interchange	
	b) American standard code for in	nformation interchange	
	c) America standard coded inter	change information	
	d) American standard coded into	erchange information	
vi.			
	a) SISD	L) CD (D	
	c) MISD	b) SIMD	
200	old section (Co.)	d) MIMD	
Vii	the permanent to	e memory	
	a) ROM	b) RAM	
	c) EPROM	d) EEPROM	
viii	What is the full form of PC regist	er	
	a) Pointer code	b) Program counter register	
	c) Perfect combination register	d) None of these	
ix	Which one is the non-impact print		
	a) drum printer	The state of the s	
	c) character printer	b) laser printer	
	Carolina An	d) none of these	
X.	If there is a mechanical contact be	tween the print head and paper then this	kind
	or britter to known as	paper then this	KIIIU
	a) Impact printer	b) Non-impact printer	
	c) Scanner	d) None of these	

Q.2	2	Shot answer (Attempt any ten questions)	[20]
	01 m	Define the terms 'Hardware' and 'Software'. Show the short method of converting Hexa-decimal number to decimal number by giving an example. Convert the following: i) (4C) ₁₆ = (?) ₁₀ ii) (101101) ₂ = (?) ₁₀ Define Logic Gates. List the basic gates. Explain with symbol and truth table (for two inputs and three inputs) the following gates: a) NAND b) XOR Draw the circuit and write the truth table for the equation: A'B+B'C+AC' What is multicomputer? Differentiate between primary memory and secondary memory. What is Seek time? What is a mouse. Describe impact printers. What is stack addressing?	
Q.3	Draw a block diagram of Basic Organization of a Computer System and explain the functions of the various units. Also, describe the evolution of computers.		
		OR	
	Wha	at is Number System? Explain the various number systems you know. Also, ain the binary addition by giving example.	[10]
Q.4	[A] [B]	Explain Hammings error detection and correction code.	[05] [05]
Q.4	[A]	i) Associative Law ii) Commutative Law iii) Distributive Law	[05] [05]
Q.5	[A] [B]	Explain in detail Array computers. Explain in detail the Von Neumann machine.	[05] [05]
Q.5	[A] [B]	OR Explain in detail pipelining. Explain the storage organization of magnetic disks.	[05] [05]
Q.6	[A]	Explain the following addressing modes: i) Immediate addressing. ii) Indexed addressing. iii) Register addressing	[06]
	[B]	Write a short note on key board. OR	[04]
Q.6	[A]	Explain the following addressing modes: i) Direct addressing. ii) Register Indirect addressing iii) Based-Indexed addressing	[06]
	[B]	Write a short note on scanner	[04]
		-2-7	