## [68]

## SARDAR PATEL UNIVERSITY

## INSTRUMENTATION - M.Sc. (SEM.-1) - NOV.-2012

## 8-BIT MICROPROCESSOR & MICROCONTROLLER SYSTEMS

SUBJECT CODE: PS01CINS02 TIME: 10:30am to 1:30p.m DATE: 03/12/2012 MARKS: 70

| Q-1 |            | Choose the correct answer from given.  |   | [80] |  |
|-----|------------|--|---|------|--|
|     | (1)        | is 16-bit register in 8085up.  | (D) shadk pointer   |      |  |
|     |            | (A) Flag register  | (B) stack pointer   |      |  |
|     |            | (C) instruction register   | (D) none of above   |      |  |
|     | (2)        | Section of 8085 decode machine code of instruction.  |   |      |  |
|     |            | (A) Instruction decoder  | (B) control   | 14   |  |
|     |            | (C) ALU  | (D) none of above   | 3    |  |
|     | (3)        | In MODE-1 of 8255 PIO, port use to transact  | handshake signal is   |      |  |
|     |            | (A) PORT-A   | (B) PORT-C  |      |  |
| 33  |            | (C) PORT-B   | (D) control register  |      |  |
|     | (4)        | MODE-2 of 8254 is described as   |   |      |  |
|     | - A - ST - | (A) Programmable one-short   | (B) rate generator  |      |  |
|     |            | (C) software triggered strobe  | (D)none of above  |      |  |
|     | (5)        |  | level of interrupt.   |      |  |
|     | (2)2       | (A) 16   | (B) 8   |      |  |
|     |            | (C) 64   | (D) 32  |      |  |
|     | (6)        | In single PIC mode of 8259 the ICWS require  | ed are  |      |  |
|     | (0)        | (A) ICW 1&2  | (B)ICW 1&3  |      |  |
|     |            | (C) ICW 2&3  | (D) none of above   |      |  |
|     | (7)        | 8051 is pin chip.  |   | 7755 |  |
|     | (0)        | (A) 24   | (B) 40  |      |  |
|     |            | (C) 20   | (D) 64  |      |  |
|     | (0)        | Following is special function register in 8051   |   |      |  |
|     | (8)        |  | (B) control register  |      |  |
|     |            | (A) accumulator  | (D)none of above  |      |  |
|     |            | (C) TCON   | (2),  |      |  |
| Q-2 |            | Short answer type questions.(attempt any seven)  |   |      |  |
| ۷.  | (1)        | State instructions of logical group of 8085.   |   |      |  |
|     | (2)        | Draw block diagram of 8255.  |   |      |  |
|     |            | Differentiate BSR mode and I/O mode of 82  | 55.   |      |  |
|     | (3)        | Draw pin configuration of 8259 PIC.  | 73k   |      |  |
|     | (4)        |  |   |      |  |
|     | (5)        | the state of the s |   |      |  |
| 5   | (6)        |  | efly discuss rotating priority mode of DMA controller 8257. |      |  |
|     | (7)        | Explain control word of 8255 PIO.  | 8051  |      |  |
|     | (8)        | State different special function registers of  |   |      |  |
|     | (9)        | State meaning of ANL A,#n and MOV Rr,A   | instructions.   |      |  |

| Q-3 | (A)     | Discuss technique of generating different control signals in 8085 system.  | [06] |
|-----|---------|--|------|
|     | (B)     | Draw functional block diagram of 8085 and briefly explain it.  | [06] |
|     | Addistr | OR OR  |      |
|     | (B)     | Discuss logical and arithmetic instructions of 8085.   | [06] |
| Q-4 | (A)     | With the help of necessary diagram discuss different operating mode of 8255.                                       | [06] |
|     | (B)     | Draw block diagram of 8259 PIC and explain ICW-1 & ICW-2 of it.  | [06] |
|     |         | OR   |      |
|     | (B)     | With the help of necessary diagram explain MODE-0 & MODE-1 of 8254 interval timer.                                 | [06] |
| Q-5 | (A)     | Explain following architectural section of 8051 microcontroller. (1) Internal memory (2) Special function register | [06] |
|     | (B)     | Draw architectural block diagram of 8051 micro controller and briefly discuss it  OR                               | [06] |
| · · | (B)     | Describe technique of serial port interfacing in 8051.   | [06] |
| Q-6 | (A)     | Discuss different data transfer and arithmetic group instructions of 8051.   |      |
|     | (B)     | (1) Write instructions to copy the byte in TCON to register R2 using at-least three<br>different methods.          | [03] |
|     | 7       | (2) Write instructions to Double the number in register R2 and put the result in registers R3 and R4.              | [03] |
|     |         | OR   |      |
|     | (B)     | (1)Write instructions to put the number 34h in register R5,R6 and R7.  | [03] |
|     |         | (2)Write note on Assembler Directives.   | [03] |
|     |         |  |      |

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