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297		SEAT No.
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## No. of Printed Pages : 2

## SARDAR PATEL UNIVERSITY

**B.Sc.INSTRUMENTATION (V)** 

SEM-III, NOVEMBER-2018 EXAMINATION

	CODE:-US03CINV02	SUB: Electronic Circuit & Optical Devi	
DATE	:-30/11/2018, Friday	TIME:-2:00 pm to 5:00 pm MARK	
Q-1	Choose correct answer		[10]
1.	The Op-amp is device that	can be used to amplify signal.	
	(A) D.C as well A.C	(C) A.C	
	(B) D.C	(D) None of above	
2.	The Resistance Measure b	etween output terminal and ground is called	
	(A) Input voltage gain	(C) CMRR	
	(B) Open loop voltage gain		
3.	The average of two base c	urrent entering into terminal is called	
	(A) Input offset Current	(C) Input Bias Current	
	(B) Input offset Voltage	(D) none of above	
4.	The differential D.C Op-An	np is mostly used to amplify output of	
	(A) High Pass filter	(C)Transducer	
	(B) Low Pass filter	(D) none of above	
5.	Current to Voltage Conver	ter Op-Amp is also known asop-Amp.	
	(A) Trans-resistive	(C) Trans-inductive	
	(B) Trans-Capacitive	(D) none of above	
6.	The Output voltage follow	red the input Voltage known as	
	(A) A.C Voltage follower	(C) D.C Voltage follower	
	(B) Differential D.C Op-Am	p (D) none of above	
7.		of pulse that is proportional to theof modulation signal.	
	(A) Amplitude	(C) Pulse	
	(B) Amplitude And Pulse	(D) none of above	
8.		otal Periodic time T=T1+T2=	
	(A) RC log [(1+A)]	(C) 2RC log[(1+A)]	
	[(1-A)]	[(1-A)]	
	(B) 2RC log [(1-A)]	(D) none of above	
	[(1+A)]		
9.		nich Separates, unwanted quantity from the signal.	
	(A) filter	(C) Active filter	
	(B) Passive filter	(D) none of above	
10.	Bandpass filter will elimin	ate	
	(A) Intermediate	(C) Zero	
	(B) Low And High	(D) none of above	
Q-2	Short answer type questi	on. (any ten)	[20]
1.	Briefly Explain Input Impe	dance And Common Mode Rejection Ratio (CMRR) of	
	operational Amplifier.		
2.	Explain Ideal Op-Amp Cha	aracteristics.	
3.		ncy Response And Bandwidth.	
4.	Draw the Circuit Diagram	of Current to Voltage Converter in Operational Amplifier.	
5.		egrator with necessary figure.	
6.		parator in Operational Amplifier.	
7.	Draw Phase Detector Circ		
8.		ave generator and briefly Explain.	
9.	Briefly Explain Schmitt Tr	igger Circuit.	•
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	10	Differentiate between Active filter Vs. Passive Filter.	
	10. 11.	Briefly explain Notch filter.	
	11. 12.	Draw the Block diagram of Op-Amp	
	12.		
	Q.3	Draw the Circuit Diagram of Inverting Amplifier and Derive necessary expression for it with feedback.	[10]
		OR	[הס]
	Q.3(A)	Describe summing op-amp with necessary diagram.	[07]
	Q.3(B)	List the D.C and A.C Parameters of Op-Amp	[03]
	Q.4(A)	Draw the circuit diagram and wave form of integrator using Op-Amp and explain in detail.	[07]
	0.4/0\	Explain D.C voltage follower Circuit.	[03]
	Q.4(B)	OR	- <del>-</del>
	0.4	Explain in Detail Voltage to Current Converter using Op-Amp.	[10]
	Q.4	Describe Astable Mulltivibrator with Diagram and Waveform s.	[10]
	Q.5	OR	
	0.5/41	Explain Zero-Crossing Detector with necessary wave form.	[07]
	Q.5(A)	Discuss Pulse with Modulation circuit.	[03]
	Q.5(B)	DISCUSS Pulse with Modulation circuit.	
	0.6	Give the details about the Data Acquisition using Instrumentation Amplifier	[10]
	Q.6	OR	- *
	0.6	Define Instrumentation amplifier and explain it with circuit Diagram	[10]
	Q.6	Define that unferration amplitues and explains it with all and all all and	- •

