T98J

SARDER PATEL UNIVERSITY. V.V. NAGER.

S.YB.Sc. Sem-IIIEXAMINATION, ELECTRONICS DEVICES

SUB.	CODE:-US03CELE21 <u>:-31/13/2020</u>	TIME:- 9::00PM to	4:00PM	MARKS-70		
<u> VAIL</u>	10012020			· · · · · · · · · · · · · · · · · · ·		
	Choose correct answ	10 K	in the second se		[10]	
Q-1		name of			• •	
1		lattie OI	(C) BJT	•		
	(A) Tunnel diode		(D) None of these			
	(B) FET (D) None of these In very heavily doped n type semiconductor conduction band isfilled.					
2.						
	(A)Completely		(D) None of these			
	(B) Empty	tor winding is	• •			
3.		tor willumg is	(C) Both			
	(A) Anti-Clock wise		(D) None of these			
	(B) Clockwise	made from glass and _	' '			
4. r	(A) Metal	made nom Blass and _	(C) Gold			
	• •		(D) None of these			
	(B) Carbon There are two types of resistors variable and					
5. 6.	(A)Fixed	Of (Calatora variable as	(C) Zero			
	• •		(D) None of these			
	(B) Not Constant (D) None of these Diode clamper circuit is used in					
	(A) Amplifier	it is asca in	— (C)Radio	* -		
	(B) TV		(D) None of these			
7.	in amplitude modul	lation of ca	rrier signal is varied as p	er audio signal.		
	(A) Amplitude		(C) Phase			
	(B) Frequency		(D) None of these			
8.		rcuit is used in	• •	* *		
٥.	(A)Calculator		(C) Sensing machines			
	(B) DC Power supply	ı	(D) None of these			
9.	Pure Semiconductor	r material is called as _				
٥.	(A) Extrinsic		(C) Insulator			
	(B) Intrinsic		(D) None of these			
10.	The barrier potential is negative on P-side andon N-side in PN Junction diode					
10.	(A) Zero		(C) Negative			
	(B) Positive		(D) None of these			
	(5) 1 0511110					
Q.2	Fill in the Blanks &	True or False			(08)	
[a]	Fill in the blanks.				(04)	
1)	The process of reco	vering the modulating	signal from modulated of	arrier signal is		
	called					
2)	Donor Doped Semi	conductor material is k	nown assen	niconductor.		
3)	The depletion region in tunnel diode is					
4)		resistance devi				
[b]	Answer True or Fal				(04)	
1)	Tantalium capacito	Tantalium capacitor is a similar to electrolytic capacitor in polarity.				
2)	Doping profile in H	Doping profile in Hyper-abrupt junction diode and abrupt junction diode is same.				
3)	Square law Diode r	nodulator uses linear p	ortion of the diode char	acteristics.		
4)	The time constant	for step response of RC	Circuit is given by T=RC.			
		ه ـ نفست	_	F 0.5 A	~	

Q-3	Short answer type question. (any ten)	[20]
1.	Define amplitude modulation.	•
2.	Draw the basic circuit of Linear Diode Detector	
3.	Define Modulation Index.	
4.	Draw the diagram of Abrupt Junction VVC diode.	
5.	Draw the diagram of Hyper-Abrunt Junction, Wyorking	
6. ¬	braw the didgram snowing principle of voltage variable assets.	
7.	The arrivate of the sellificonductor	
8. 9.	write a note on N-type semi-conductor	
9. 10.	Draw the diagram of wire wound reisistor	
11.	Give full form of CCD.	
12.	Draw a neat circuit of full wave rectifier using two diodes.	
12.	Define amplitude demodulation.	
Q.4	Answer any four out of eight question	
011		32
Q.(a)	Give an account of step response of RL circuit.	Fo.)
Q.(b)	•	[8]
α,(ρ)	Explain working of Forward-Biased and Reverse-Biased PN Junction Diode in detail.	[8]
	detail.	(o)
Q.(c)	Explain any two in detail	
	(1) Peak Rectifier	[8]
	(2) Voltage Doubler	. ,
	(3) Diode Clamper	
	(4) Diode Limiter	
•		
Q.(d)	List different types of fixed resistors and explain any two in detail.	
	and explain any two in detail.	[8]
0 (-)	14	
Q.(e)	List different type of Fixed Capacitors and explain any two in detail.	ro.
Q.(f)		[8]
Q.(i)	Write a note on voltage variable capacitor diode.	[0]
Q.(g)		[8]
7/10/	Write a note on thermistor and draw circuit of its one application.	[8]
Q.(h)		(~)
•	Give an account of Amplitude Modulation.	[8]
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