SARDAR PATEL UNIVERSITY

S.Y.B.Sc: SEMESTER - III

US03CCSC02: COMPUTER ORGANIZATION

Thursday, 21st November - 2013

l'ime	e: 02:30pm to 05:30pm		Max.Marks: 70		
Q.1	Multiple choice of Question			10	
	[1] CPU stands for(a) Control Processing Unit(c) Control Programming U	nit	(b) Central Processing Unit (d) None of above		
	[2] In Hexadecimal Number sys				
	(a) 09 (b) 10 [3] In Octal Number system, ba	(c) 15	(d) None of above		
	(a) 8 (b) 7	(c) 9	(d) None of above		
	[4] ASCII equivalent of A is	(0) 9	(u) None of above		
	(a) 66 (b) 65	(c) 68	(d) 67		
	[5] ASCII equivalent of 9 is	(0) 00	(a) 07		
	(a) 56 (b) 57	(c) 58	(d) 59		
	[6] Extra bit added to a string of	of bits to	letect errors is known as		
	(a) Additional bit	(b) Cor	rection bit		
	(c) Parity bit	(d) upo	lating bit		
	[7] The Full form of RAM is.				
	(a) Read only memory	(b) Read and write memory			
	(c) Random access memory	(d) Rea	ally a memory		
	[8] The full form of RAID is (a) Redundant Array of inex (c) Rapid access in disk		isk (b) Read Array in disk (d) None of these		
	[9] Which one is the input device				
	(a) Printer	(b) Sca			
	(c) Plotter (d) none of them				
	[10] Which one is output device				
	(a) keyboard	(b) Sca			
	(c) mouse	(d) plot	ter		
Q.2	Attempt any 10 questions			20	
	[1] Define the terms 'Hardware' and 'Software'.				
	[2] List out the limitations of Primary Storage.				
	[3] Explain the conversion of Decimal to Octal method.				
	[4] Explain 1's complement method with example.				
	[5] List stages of pipelining				
	[6] Explain multifunction unit.				
	[7] What is ROM?				
	[8] What do you mean by PROM?				
	[9] What is Instruction register?				
	[10] Differentiate: input device and output device.				
	[11] Define line printer.				
	[12] What is keyboard?				

Q.3	[A] Explain Input Unit with its functions.	5	
	[B] Write a brief note on Evaluation of Computers.	5	
	\mathbf{OR}		
Q.3			
	with suitable examples.		
Q.4	[A] Explain CPU organization with diagram.[B] Explain multiprocessor.	5 5	
	OR		
Q.4	[A] Write a note on Vonn Neumann machine [B] Explain array processor with diagram	5 5	
Q.5	[A] Explain Flash drive.[B] Explain Various types and organization of secondary storage device.	5 5	
	OR		
Q.5	[A] Explain Hard Disk with diagram.[B] Explain the block diagram of Processor and memory architecture of a computer system.	5 5	
Q.6	[A] Explain monitor by drawing CRT.[B] Write a note on Mouse.	5 5	
	$ ho_{\mathbf{R}}$		
Q.6	Explain all Addressing Technique in detail with examples.		
	The second section of the second section is a second section of the second section of the second section is a second section of the second section of the second section secti		

