No. of Printed/Pages: 2 SC

SARDAR PATEL UNIVERSITY

Vallabh Vidyanagar - 388120

B.Sc. (6th Sem) Examination - March/April-2018 [CBCS]

Monday, 2nd April, 2018,

10:00 AM - 01:00 PM

US06CINS04 (Instrumentation)

Programmable Logic Controller (PLC) - 2

| | | 0 | Maximum Mar | ks: 70 | | |
|-------|---|---|--|--------|--|--|
| Que 1 | Each question below g | gives a multiple choice o | f answers. Choose the most appropriate | [10] | | |
| 1 | : Consists of One R | Rung of Logic, which is Pl | aced at the End of The Ladder Diagram. | | | |
| | a) D Flip Flop | | One Shot | | | |
| | c) RS Flip Flop | d) | T Flip Flop | | | |
| 2 | : Supply Clock (Trigger) Pulse For the D, T, and J-K Flip Flops. | | | | | |
| | a) Timer | | Sequencer | | | |
| | c) One Shot | d) | Flasher | | | |
| 3 | _ | r Ladder Function tha a Precise Internal Clock | t Allows the PLC to Perform Timing | | | |
| | a) Timed Sequencer | b) | Timed One Shot | | | |
| | c) Flasher | d) | Timer | | | |
| 4 | In Mnemonic PLC Prois Generally | ogramming Code, the Te | rmination Command For a Line of Code | | | |
| | a) STO | b) | ,LD | | | |
| | c) ENTER | d) | STR | | | |
| 5 | | ogramming Code, the _ Contacts Composes One | Command Tells the Controller That Branch of Logic. | | | |
| | a) LD | b) | STO | | | |
| | c) STR | - | ENTER | | | |
| 6 | In Mnemonic PLC Pro the Previous Logic is | ogramming Code, the _ To Control the Coil Tha | Command Tells the Controller That t Follows the STO Command. | | | |
| | a) STR | b) | ENTER | | | |
| | c) LD | d) | STO | | | |
| 7 | The Inputs of Moderr | n PLCs are Generally | | | | |
| | a) 240 AC Voltage | • | 24 DC Voltage | | | |
| | c) 120 AC Voltage | d) | Opto-Isolators | | | |
| 8 | Relay Contact is | a Single Pole Normally | | | | |
| | a) FORM A | - | FORM B | | | |
| | c) FORM C | | FORM A and FORM C | | | |
| 9 | If the Voltage Being Me | | rative, Then Input is the Best Choice. | | | |
| • | a) Unipolar (Positive | omy) | Bipolar | | | |
| | c) Unipolar and Bipol | ui - | Unipolar (Negative Only) | | | |
| 10 | For a Unipolar Converter, the Voltage Resolution is the Full Scale Voltage Divided by, where n is the Bit Resolution. | | | | | |
| | a) 2n-1 | | 2n | | | |
| | c) 2n ² | d) | 2n+1 | | | |
| | | | (P.T.O.) | | | |

| Que 2 | Short Questions (Attempt any TEN) | | | | |
|-------|---|--|------|--|--|
| 1 | Draw Truth Table and Ladder Diagram For RS Flip Flop. | | | | |
| 2 | Explain Briefly Flasher. | | | | |
| 3 | Explain the Importance of Cascading of Timers. | | | | |
| 4 | Write Mnemonic PLC Programming Codes For $Lamp = \overline{Sensor A} + Sensor B$. | | | | |
| 5 | Write Mnemonic PLC Programming Codes For $Y = \bar{A}B + C$ | | | | |
| 6 | Write Mnemonic PLC Programming Codes For OUT10= IN1. IN2. IN3 | | | | |
| 7 | Explain PLC Output Wiring Briefly. | | | | |
| 8 | Draw Diagram of PLC Non-Isolated Input Wiring. | | | | |
| 9 | Differentiate: Relay Outputs and Solid State Outputs Associated With PLC Wiring. | | | | |
| 10 | Differentiate: Unipolar and Bipolar Inputs. | | | | |
| 11 | A 12-Bit, 10 Volt Bipolar Analog Output has a Maximum Output Current Capability of 20 mA. It is Connected to a Load that has a Resistance of 330 ohms. Will this System Work Correctly? | | | | |
| 12 | A voltage of 3.500 Volts is Applied to an 8 - bit, 5 volt Unipolar Analog Input of a PLC. The PLC Analog Input Register shows a Value of 2638. Is the Analog Input working correctly? | | | | |
| Que 3 | [A] | Explain Counter With Necessary Ladder Diagram. | [06] | | |
| | [B] | Write a Note on Timed One Shot. | [04] | | |
| | | OR | | | |
| | [C] | Explain Timer With Necessary Ladder Diagram. | [06] | | |
| | [Đ] | Discuss D Flip Flop Associated With PLC With Necessary Truth Table and Ladder Diagram. | [04] | | |
| Que 4 | [A] | Write a Detailed on PLC Simple Branches With Necessary Example. | [06] | | |
| | [B] | Discuss AND Ladder Rung With Necessary Ladder Diagram and Mnemonic Programming Codes. | [04] | | |
| | | OR | 1061 | | |
| | [C] | Write a Detailed on PLC Complex Branches With Necessary Example. | [06] | | |
| | [D] | Discuss OR Ladder Rung With Necessary Ladder Diagram and Mnemonic Programming Codes. | [04] | | |
| Que 5 | [A] | Write a Detailed Note on PLC Relay Outputs. | [10] | | |
| | | OR | [1/] | | |
| | [B] | Give an Account of Inputs Having a Single Common in Terms of PLC. | [10] | | |
| Que 6 | [A] | Write a Note on Analog (A/D) Input With Respect to Programmable Logic Controller. | [06] | | |
| | [B] | Give an Account of Analog (D/A) Output. | [04] | | |
| | | OR | [NZ] | | |
| | [C] | Write a Note on Analog Input Potential Problems With Respect to Programmable Logic Controller. | [06] | | |
| | [D] | Write a Note Analog Data Handling. | [04] | | |