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## [54]

## Seat No: \_\_\_\_\_ No. of Printed F SARDAR PATEL UNIVERSITY V.V.NAGAR

B.Sc. (V<sup>th</sup> SEM.) INSTRUMENTATION(VOC.) 20<sup>th</sup> NOVEMBER-2019 EXAMINATION

## 8-BIT MICROPROCESSOR PROGRAMMING AND APPLICATION-1 SUB.CODE-US05CINV05

	:-10:00 am to 1:00 pm	A STATE OF THE STA	1101		
Q-1.	Choose correct answer	CELL offer execution of DAD	[10]		
1.	The content of accumulator is CF H, after execution of RAR				
	instruction it becomes				
	(A) 55 H	(C) A5 H			
	(B) AA H	(D) none of above			
2.	Flag is affected during data transfer operation.				
	(A) Carry	(C) Zero			
	(B) Sign	(D) none of above			
3.	In 8085 up INX instruction is _				
	(A) 1	(C) 3			
	(B) 2	(D) none of above			
4.	The address buses of 8085 µp contain bit.				
	(A) 8	(C) 16			
	(B) 7	(D) none of above			
5.	is machine control instruction.				
	(A) NOP	(C) JC			
	(B) HLT	(D) none of above			
6.	RRC is type instruction				
	(A) Data transfer	(C) Logical			
	(B) Branch	(D) none of above			
7.	RET is byte instruction.				
	(A) One	(C) Three			
	(B) Two	(D) none of above			
8.	Which of following is three by	te instruction?			
	(A) MVI B,0A H	(C) JC 2A00 H			
	(B) MOV B,A	(D) none of above			
9.	An 8085 Microprocessor requ	uire power supply.			
	(A) 15 V	(C) 10 V			
	(B) 5 V	(D) none of above			
10.	8085 up has types of i				
	(A) 5	(C) 3			
	(B) 4	(D) none of above			
Q-2	Short answer type questio	n. (any ten)	[20]		
1.	Briefly Explain: Why data bus				
2.	Briefly explain program and software.				
3.	State characteristics of logical instruction.				
4,	Explain HLT instruction.				
5,	State meaning of RRC and RLC with illustration.				
6.	What do you mean looping and counting technique?				
7.	Define static and dynamic del				
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8. What is ALU? And explain its function. 9. Briefly explain DCX and DCR instruction. 10. State two byte instructions. List pins of interrupt control section of 8085 microprocessor. 11. 12. List different type of flag register. Q.3 Draw the architectural block diagram of 8085 µp and explain [10] function of each section in detail. Explain the following: A) Bus timing, B) De-multiplexing the Q.3 [10] bus AD<sub>0</sub>- AD<sub>7</sub> Q.4(A) Explain classification of instruction 0f 8085  $\mu p$ . [06] Q.4(B) State different types of addressing mode of 8085 µp with [04] illustration. OR Q.4(A) Explain the method of writing, assembling and executing a [07] simple program in 8085 μp. Q.4(B) Briefly explain op-code and operand of 8085 up. [03] Q.5(A) Discuss different Arithmetic instructions with suitable [07] illustration. Q.5(B) Write a programme: to load 3B H and 9A H in register C and [03] D respectively. Now increment content of C than add both the number and display the sum at output port. OR Q.5(A) Explain Different Logical instructions with suitable example of [07] each. Q.5(B) Write a programme to load two numbers in two registers now [03] Add one number from other such that carry flag will set and display the answer at output port. Q.6(A) Explain conditional and un-conditional jump instructions [05] giving suitable examples. Q.6(B) Write a program to load 47 H and A7 H in register B and C [05] respectively. Now add both the numbers, if the sum is greater than FF H display 01 at output port 0, otherwise display the sum. OR Discuss different additional data transfer instructions and 16- [10] Q.6 bit arithmetic instructions with illustration of each.