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No. of printed pages: 2

## SARDAR PATEL UNIVERSITY S. Y. B.C.A. (III SEMESTER) EXAMINATION Friday, 25 NOVMBER, 2016 2.00 P.M to 4.00 P.M

US03EBCA01: Introduction to Microprocessors

		•		lotal Marks	s : 70		
Q-1.	1			•	[10]		
1.	A flag is a one type of						
	(A) Latch	(B)	Gate				
_	(C) Flip-Flop	(D)	None of these				
2.	An assembler translates the assembly language to the binary codes.						
	(A) Program	(B)	mnemonics	•	*		
	(C) Code	(D)	None of these	ž.			
3.	The assembler list file extension is		`.				
	(A) .OBJ		LST				
	(C) .LIS	, ,	None of these				
4.	DIV stands for	. ,					
	(A) Division	(B)	Signed Division				
	(C) Unsigned Division		None of these				
5.	NOT stands for			-			
	(A) Negative	(B)	Nothing				
	(C) Invert all Byte	(D)	Invert each Rit				
6.	ROL stands for Rotate of operand left.						
	(A) each Bits		each Bytes				
	(C) all Bits	(D)	all Bytes				
7.	JZ is executed when the zero flag is	s	,				
	(A) reset	(B)	set				
	(C) undefined	(D)	None of these				
8.	SHL stands for Shift left						
	(A) Operand Bits	· (B)	Bits				
	(C) Operand Bytes	(D)	Bytes				
9.	An END directive ends the	<b></b>		. *	,		
	(A) segment	(B)	program				
	(C) procedure	(D)	None of these	·			
10.	In WHILE-DO structure the condition is checked any action is done.						
,	(A) after	(B)	before				
	(C) in the structure	(D)	None of these				

Q-2.	Short Question. ( Any Ten )		[20]			
1	Wh					
2	List					
3	Wh					
4		State the example of SBB instruction with syntax.				
5	Explain NOT instruction with example.					
6	State the example of DIV instructions with syntax and full form.					
7	Differentiate ROL and ROR.					
8	Wh					
9	Exp	•				
10	Wh					
11	Wri					
12	Diff	erentiate WHILE-DO and REPEAT-UNTIL.				
Q-3.	[A]	Explain all major directions in microprocessor evaluation.	[5]			
	[B]	Explain EU in detail.	[5]			
		OR	<b>2</b> 2			
Q-3.	[A]	Explain BIU in detail.	[5]			
	[B]	What is an Assembler? Explain the concept of assembler.	[5]			
Q-4.	[A]	Explain MOV & CMP instructions in detail with example.	[5]			
	[B]	Explain INC & DEC instructions in detail.	[5]			
		OR	• • ·			
Q-4.	[A]	Explain Logical instructions in detail.	[5]			
	[B]	Explain ADC instructions with example.	[5]			
Q-5.	[A]	Write a short note on Rotate instructions ROL and ROR with example.	[5]			
	[B]	Explain JNC instructions with example OR	[5]			
Q-5.	[A]	Write a short note on Shift instructions SHL and SHR with example.	[ <b>6</b> ]			
٠, ٠.	[B]	Explain JZ instructions with example	[5]			
	f-1	Explain 62 moraotiono mai example	[5]			
Q-6.	[A]	Explain structure of assembly program.	[5]			
	[B]	List control structures. Explain any one in detail.	[5]			
		OR				
Q-6.	[A]	Which are the Looping structures? Explain any one in detail.	[5]			
	[B]	Explain DB & END directives in detail	121			