SARDAR PATEL UNIVERSITY B.C.A. - SEMESTER - I EXAMINATION US01CBCA25 - FUNDAMENTAL OF COMPUTER ORGANIZATION

Date :	: 24-10-2 Wedne			Time : 10:00 a.m. t	o 1:00 j	p.m. Marks : ?	70			
Q-1	: 5		t an appropriate option for following questions.							
		1)	Which parts	of the computer perform	n arithi	netic calculations?				
			A. A	LU	B.	Registers				
			C. L	ogic bus	D.	Input device				
		2)	Which staten	nent is valid?		•				
		•		KB = 1024 bytes	В.					
		•		MB = 1000 kilobytes	D.					
		3)	The represent	tation of – 18 using 1's	compl	ement is				
			A.	11101101	В.	11001101				
			C.	10001010	D.	10110011				
•		4)		alent of B is		•				
			. A.	66	В.	67				
			C.	68	D.	65				
		5)		001 then odd parity for						
			A.	11000001	В.	01000001				
			C.	10000001	D.	01001000				
		6)	Multiprocess	or is referred as						
			A.	SISD	В.	SIMD				
			. C.	MISD	D.	MIMD				
		7)	Cache memo	ry is being deployed fo						
			Α.	Speed up rate of data						
			В.	reduce rate of data f	etching					
			C	to reduce the error		•				
			D.	none of them		•				
		8)	What is the fi	all form of USB	•					
			A.	Universal Serial Bus	В.	Universal system Bus				
			, C.	uniform system bus	D.	none of them				
		9)	If there is a m	nechanical contact betw	veen the	print head and paper then				
			this kind of p	rinter is known as	·	•				
			A.	Impact printer	B.	non-impact printer				
			C.	normal printer	D.	none of these				
		10)	Which one is	a pointing device	·					
			A.	scanner	В.	keyboard				
			C.	Mouse	D.	none of these				
			0.11 1 0				F 0 03			
Q-2				uestions : (Any ten)			[20]			
	,		Hardware and		0					
	•		ain characteristics of fourth generation of computer.							
	,	-	ain Odd and even parity using appropriate example.							
	4) F		the blank							
		1)	$(127)_{10} =$	() ₂ = () ₁₀						
•	#\ T	11)	(AB3) ₁₆ =	= ()10						
	5) V	vrite	a oriet note on	Data path cycle.						

•				
		6)	What will be the result for following, i) $(1101101)_2 + (1101)_2$ ii) $(110111)_2 - (1101)_2$ using 1's complement method	
	,	7) 8) 9) 10)	List stages of pipelining. State significance of Program Control Register and Instruction Register. Define Index and stack addressing. List various input output devices (at least four).	
		11) 12)	State difference between RAM and ROM. Explain importance of memory Buffer Register (MBR).	
	Q-3	A) B)	Draw the block diagram of computer and explain its functions. What is number system? Convert Decimal number (2345) into Binary, Octal and Hexadecimal Number system.	[5] [5]
•			OR	
	Q-3	A) B)	List applications of Computers. Compare various generations of Computers. What is base value of Binary number system? Convert (234.12) ₈ into Binary, Decimal and Hexadecimal Number system.	[5] [5]
	Q-4	A) B)	Write a note on Excess notation. Write a note on Von Neumann Machine. Explain Instruction Execution cycle.	[5] [5]
	Q-4	A) B)	OR Explain Error correction code with appropriate example. Write a note on Character Codes.	[5] [5]
	Q-5	A) B)	Write a note on Primary memory. Write a note on Array Processors.	[5] [5]
. ,			OR	
	Q-5	A) B)	Write a note on Multiprocessors. Write note on Secondary storage devices.	[5] [5]
	Q 6	A)	What is addressing? Explain various addressing techniques in detail.	[10]
	Q 6	A)	OR Write a detailed note on various types of Printers.	[10]
			—X —	
· .			2	. .
•			en e	
	•			•
	•			