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SEAT No. \_\_\_\_\_

No. of Printed Pages : 1

**Sardar Patel University**

BBA(ISM) Examination (I Semester) NC BATCH 2011

Wednesday, 24th October 2018

02.00 – 04.00 PM

**UM01CBBS02- LOGICAL ORGANIZATION OF COMPUTER**

**Total Marks: 60**

- Q1. A. Explain block diagram of computer in detail. 07
- B. Convert the following exam 06
  - 1. Convert hexadecimal to octal  $(15)_{16}$
  - 2. Convert octal to hexadecimal  $(20)_8$
- C. What do you mean by Error Detection method? 02
- OR**
- Q1. A. Explain different functions of Computer in detail. 06
- B. Explain 1's complement method. 05
- C. What do you mean by Signed magnitude method? 04
- Q2. What is use of Gate? Explain all gates with circuit and Truth Table. 15
- OR**
- Q2. Explain the following method in detail. 07
  - 1. De'morgan Theorem (Both) 08
  - 2. Karnuagh Map method with 3 variables
- Q3. Explain the following method in detail. 07
  - 1. Function of processor 08
  - 2. Von Neumann Computer Model
- OR**
- Q3. Explain the following method in detail. 07
  - 1. Instruction Execution Cycle for CPU 08
  - 2. Array Processors
- Q4. A. List various Input Devices and explain them in brief. 08
- B. Explain the Concept of CD-ROM. 07
- OR**
- Q4. A. List the various Output Devices and explain them in brief. 08
- B. Explain the concept of Hard Disk. 07

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