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SARDAR PATEL UNIVERSITY
F.Y.B.B.A. (ISM) SEM - I EXAMINATION
2013

Saturday, 16th November

02.30 p.m. to 04.30 p.m.

LOGICAL ORGANIZATION OF COMPUTER -I: UM01CBBS02

Maximum Marks: 60

Q.1

- A. Explain Block Diagram of Computer in detail. (6)
- B. Explain 2's Complement method. (5)
- C. What do you mean by Error detection code? (4)

OR

Q.1

- A. Explain different functions of Computer in detail. (6)
- B. Explain 1's Complement method. (5)
- C. What do you mean by signed magnitude method? (4)

Q.2

- A. What is Truth table? Explain AND and OR Gates with example. (7)
- B. Explain Karnaugh Map for 3 variables in detail. (8)

OR

Q.2

- A. What is Truth table? Explain NAND and NOR Gates with example. (6)
- B. What is Boolean Algebra? List and explain laws of Boolean Algebra. (5)
- C. Explain De Morgan's Theorem in detail. (4)

Q.3

- A. Explain the following:
 - 1. Instruction Execution Cycle for CPU (7)
 - 2. Data path of a typical Von-Neuman machine parallel (8)

OR

Q.3

- A. Explain the following:
 - 1. Array Processors (7)
 - 2. Processor function and its components (8)

Q.4

- A. List the types of Memory Systems for Microcomputer and explain ROM in detail. (6)
- B. Explain the concept of Hard Disc. (4)
- C. List out various input output devices and explain Keyboard in detail. (5)

OR

Q.4

- A. List the types of Memory Systems for Microcomputer and explain RAM in detail. (6)
- B. Explain the concept of CD-ROM. (4)
- C. List out various input output devices and explain Printer in detail. (5)

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Y. K. M.