

SARDAR PATEL UNIVERSITY
F.Y.B.B.A (ITM) Examination, Semester – I
Computer Organization
UM01CBB103

Date: Monday 18th November, 2013

Time: 02:30 p.m. to 04:30 p.m.

Total Marks: 60

- Q. 1 [A] Explain Binary Number System, Octal Number System and Hexadecimal Number system in detail [09]
- [B] Solve the Following Problems: [06]
1. $(123)_8 = (?)_{10}$
 2. $(100101)_2 = (?)_{10}$
 3. $(1BB)_{16} = (?)_8$

OR

- Q. 1 [A] List & Explain storage representation of Integers with its examples in detail [09]
- [B] Explain Parity Bit Method and Hamming Code in detail [06]

- Q. 2 Explain Parallel Instruction Execution in detail [15]

OR

- Q. 2 [A] Define: Addressing. Explain Direct & Indirect Addressing with suitable example. [12]
- [B] List out the steps of Instruction Execution Cycle [03]

- Q. 3 [A] List out Input / Output devices and Memory devices. Explain Keyboard in detail. [10]
- [B] Write a note on: Hard Disk [05]

OR

- Q. 3 [A] Define: Memory. Explain Primary Memory in detail [09]
- [B] Define: Scanner. List out various scanner and explain any one of them [06]

- Q. 4 [A] What is Instruction Set? Explain LDA and ADD Instructions in brief. [09]
- [B] Discuss the following Instructions. [06]
1. Program Counter
 2. Output Register
 3. Instruction Register

OR

- Q. 4 [A] Describe Execution Cycle in detail [09]
- [B] Discuss the following Instructions. [06]
1. Input and MAR
 2. Accumulator
 3. Binary Display

Best of Luck