

SARDAR PATEL UNIVERSITY
Programme: B.Sc
Semester: IV
Syllabus with effect from: November - 2012

Paper Code: US04CELE03	Total Credit: 3
Title of Paper: Practical	

Unit	Description in detail	Weightage (%)
	JFET Characteristics	
	FET voltage amplifier	
	Fixed voltage biasing using FET / BJT / MOSFET	
	Self biasing using FET/ BJT / MOSFET	
	Potential divider biasing using FET/ BJT / MOSFET	
	XOR, XNOR gate applications	
	Half and Full Adder	
	Half and Full subtracter	
	Flip-flops (RS, Clocked RS, D, JK Flip – flops)	
	Schmitt trigger using transistor	
	Astable multivibrator using transistor	
	Monostable multivibrator using transistor	
	Asynchronous counter (MOD 16, 8, 7)	
	Synchronous counter (MOD 16, 8, 7)	
	BCD counter	
And other experiments based on syllabus		

