SARDAR PATEL UNIVERSITY
Programme & Subject: B.Sc (Instrumentation – Vocational)
Semester: V
Syllabus with Effect from: June-2013

Paper Code: US05CINV08	**Total Credit: 2**
Title Of Paper: Practical	

Unit	Description in detail	Weightage (%)
	Digital to analog converter (DAC, R-2R)	
	Digital to analog converter (DAC, weighted Register)	
	Analog to digital converter (ADC, successive approximation)	
	Analog to digital converter (ADC, counter type)	
	Analog to digital converter (ADC, dual slope)	
	Introduction to 8085 μp	
	8 bit Arithmetic addition in 8085 μp	
	8 bit arithmetic subtraction in 8085 μp	
	8 bit logical operation in 8085 μp-I	
	8 bit logical operation in 8085 μp-II	
	8 bit data transfer in 8085 μp	
	Branch operation in 8085 μp	