



SARDAR PATEL UNIVERSITY

Vallabh Vidyanagar

NAAC 'A' Grade (10-01-2023 To 09-01-2028)

NEP-2020 aligned Curriculum with effect from Academic Year 2026-27

PG Diploma (Semiconductor Science and Technology)

Semester-I

Course Type	Course Code	Course Title	Teaching-Learning Scheme		Credits	Assessment Scheme & Marks				
			L-P-T	Total Notional Hours		Internal Assessment			External Assessment	Total Marks
						FA	SA	Total		
DSC	PGD01CSST01	Semiconductor Ecosystem, Devices and Technology	4-0-0	120	4	25	25	50	50	100
DSC	PGD01CSST02	Fundamentals of Analog and Digital Circuits	4-0-0	120	4	25	25	50	50	100
DSC	PGD01CSST03	Verilog HDL for Digital Design and Synthesis	4-0-0	120	4	25	25	50	50	100
DSC	PGD01CSST04	Practical-1	0-8-0	120	4	25	25	50	50	100
DSC	PGD01CSST05	Practical-2	0-8-0	120	4	25	25	50	50	100
DSC	PGD01CSST06	Research Project	0-2-0	60	2	15	10	25	25	50
				660	22	140	135	275	275	550

DSC: Discipline Specific Core

DSE: Discipline Specific Elective

L: Lecture

P: Practical

T: Tutorial

FA: Formative Assessment

SA: Summative Assessment



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Semester-II

Course Type	Course Code	Course Title	Teaching-Learning Scheme		Credits	Assessment Scheme & Marks				
			L-P-T	Total Notional Hours		Internal Assessment			External Assessment	Total Marks
						FA	SA	Total		
DSC	PGD02CSST01	Advanced Semiconductor Devices, Packaging and PCB Design	4-0-0	120	4	25	25	50	50	100
DSC	PGD02CSST02	Semiconductor Fabrication and Characterization Techniques	4-0-0	120	4	25	25	50	50	100
DSC	PGD02CSST03	Nanofabrication and Device Simulation	4-0-0	120	4	25	25	50	50	100
DSC	PGD02CSST04	Practical-1	0-8-0	120	4	25	25	50	50	100
DSC	PGD02CSST05	Practical-2	0-8-0	120	4	25	25	50	50	100
DSC	PGD02CSST06	Research Project	0-2-0	60	2	10	15	25	25	50
				660	22	135	140	275	275	550

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