



SARDAR PATEL UNIVERSITY

NOTIFICATION NO: E-1 OF 2017

EXAMINATION PROGRAMME, NOVEMBER - 2017

M.SC. (ELECTRONICS & COMMUNICATION)

IIIrd SEMESTER UNDER (CBCS)

Important Instruction:

- (1) Candidates are requested to remain present at their respective place of examination, fifteen minutes before the time of commencement of the first paper and ten minutes before the time of commencement of each subsequent paper.
- (2) Candidates are forbidden from taking anything except pens, pencils, rubbers, stencils, calculator and drawing instruments into the examination hall. Under no circumstances loan or exchange of the above materials will be allowed in the examination hall.
- (3) **Mobile Phone is Strictly Not Permitted in the Examination Hall. Seating in examination hall with any electronic gadgets will be treated as Copy Case (U.F.M. Case)**
- (4) **Students have to follow the instructions given in the answer book.**
- (5) **Note for practical examination :**
The Practical Examination will be held before or after the conclusion of the written examination at the College concerned. The exact dates and time of the Practical examination will be fixed by the Principal of the College Concerned. Due notice will be given on the NOTICE BOARD of the College. Candidates are requested to remain present at the date, time and place notified for their practical examination. Those who fail to remain present themselves at the date; time and place notified will not be examined afterward.
- (6) The University reserves the right to cancel/change date, time & place of examination without assigning any reasons. However, any change in the examination programme will be notified on the university website.

Place of Examination : As per exam center mentioned in your hall ticket

Time of Examination : 02:00 P.M. TO 05:00 P.M.

The written examination will be conducted in the following order:

DATE & DAY	COURSE CODE	COURSE TITLE
02-11-2017 Thursday	PS03CELC01	Satellite Communication
06-11-2017 Monday	PS03CELC02	Digital Signal Processing
08-11-2017 Wednesday	PS03CELC03	Control System
10-11-2017 Friday	PS03EELC01	CMOS Technology and VLSI Design

NO. : E-1/3669

VALLABH VIDYANAGAR

DATE :05/09/2017

DEPUTY REGISTRAR